EUROfusion Project Committee for High Performance Computing (HPC) Systems and Related Infrastructure

Terms of Reference

Purpose

The **HPC Project Committee (PC)** is established by the EUROfusion Programme Manager to support him in overseeing the implementation and exploitation of HPC systems and related infrastructure within EUROfusion. The PC provides strategic direction and ensures that these infrastructures are operated efficiently, securely, and in alignment with EUROfusion's scientific and operational objectives.

Remit

The HPC PC shall support the Programme Manager by providing strategic oversight and governance for all HPC systems and related infrastructure exploited within EUROfusion. At present, the following components are part of the ecosystem established within the consortium:

- **PITAGORA HPC system**

- **EUROfusion Gateway (EFGW) computing cluster**

- **Long-Term Data Storage Facility (LTDSF)**

- **DevOps software development platform**

Mandate

The HPC PC shall:

1. Approve the **Terms of Reference of the HPC Operations Committee** responsible for the day-to-day operational monitoring of the HPC systems and related infrastructure.

2. Set **guidelines for the allocation of computing and storage resources**, including policies governing access and prioritisation.

3. Approve the **Terms of Reference of the HPC Allocation Committee**, which is responsible for the allocation of computing resources.

4. Establish the compliance of services provided by **ENEA/CINECA** and **IPPLM/PSNC,** respectively, with the obligations defined in the relevant **applicable agreements**, based on reports submitted by the HPC Operations Committee.

5. Decide on **appropriate corrective actions** in the event of deviations from agreed performance, operational commitments, or contractual obligations.

6. Review and approve **deliverables associated with the infrastructure operation and development** based on recommendations prepared by the HPC Operations Committee, including the validation of related **payments to ENEA/CINECA and IPPLM/PSNC**, respectively, in accordance with the applicable agreements.

7. Provide **recommendations to the EUROfusion Programme Management Unit (PMU)** regarding potential adjustments to the **spending profile**, particularly in cases where current or anticipated changes in **electricity prices** or other operational costs may affect the indicative spending plan.

Meetings and Procedures

1. The HPC PC shall meet **at least three times per year**.
2. Meetings shall normally take place:
   - via **videoconference**, or
   - **in person at CINECA, Bologna**, or **PSNC, Poznan**, with the possibility of remote participation.
3. If a meeting is to be held at an alternative location, the decision shall generally be taken **during the preceding meeting**.
4. The **Secretary of the HPC PC** shall prepare and circulate **minutes of each meeting**, documenting discussions, decisions, and action items.
5. Meetings of the HPC PC are led by the Chair or Deputy Chair and attended by its members and the Secretary. In addition, representatives of ENEA, CINECA, and PSNC may be invited as guests.

Membership

The HPC PC shall consist of the following members:

- Representatives from the EUROfusion PMU
- HPC Experts
- Fusion Scientists

Reporting

The HPC PC shall report to the EUROfusion Programme Manager and provide recommendations in line with its mandate, including matters concerning operational performance, financial planning, and strategic direction of EUROfusion's HPC infrastructure.

Composition of the HPC PC as of March 2026

1. Members

Representatives from the EUROfusion PMU

- Frank Jenko (DSO Head) – Chair
- Richard Kamendje – Deputy Chair

HPC Experts

- Sergi Girona (CIEMAT/BSC)
- Erwin Laure (MPG/MPCDF)
- François Robin (CEA)

Fusion Scientists

- Gilles Fourestey (EPFL)
- Aaro Järvinen (VTT)
- Xavier Litaudon (CEA)
- M.J. Pueschel (DIFFER)
- Eric Sonnendrücker (MPG)

2. Invited Guests

- Representatives of ENEA
- Representatives of CINECA
- Representatives of PSNC

3. Secretary

- Denis Kalupin (DSO Coordination Officer)