# IRfm



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# **Electronics reliability in the nuclear fusion radiation environment of tokamaks**



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# Outline

- 1. Background : nuclear fusion with D-T tokamaks
- 2. Electronics exposed to radiation in tokamaks
- 3. Radiation in tokamaks
- 4. Neutron interactions with material of semiconductor components
- 5. Radiation effects on electronics in tokamaks
- 6. How to ensure electronics reliability in tokamaks radiation environment
- 7. Validation of SER prediction method & models under DD and DT plasma neutrons

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## **1 – Background : nuclear fusion with D-T tokamaks**

#### 1.1 – Deuterium-tritium (D-T) reaction

D-T is the easiest fusion reaction to perform (T ≈ 100 million K "only")

Fusion reaction is intrinsically safe (no divergence). No greenhouse gases. No fission products. No risk of proliferation.

#### 1.1.1 – Energy release



#### 1.1.2 – Fuel and ashes

Because  $T_{1/2} = 12.3$  y, there is no natural resource of tritium => tritium must be produced.



Actual fuel : D (33 g/m<sup>3</sup> sea water => billion years) and <sup>6</sup>Li (0.17 g/m3 sea water => million years). Ashes : <sup>4</sup>He



- Poloidal + toroidal magnetic field coils => helical magnetic field on which ions are trapped => magnetic cage.
- **Plasma current** is needed for stability
  - Obtained by transformer (primary = poloidal coils, secondary = plasma) => pulsed mode
  - > Also obtained by radiofrequency current drive. Could be also obtained by "bootstrap".
- Heating: ohmic (plasma current), then micro-waves and injection of accelerated ions (neutralized before entering the plasma)

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#### **1.3 – Electricity production by D-T tokamaks**

Neutrons from DT reaction leave the magnetic cage which confines the plasma (because they are neutral) and release their energy (4/5 of the energy of the reaction) in the heat exchangers (plasma facing components) of the primary cooling circuit.



Neutrons leaving the magnetic cage will hit **breeding blankets** (plasma facing components) to produce Tritium (<sup>6</sup>Li + n  $\rightarrow$  <sup>4</sup>He + T).

Hot water from the secondary cooling circuit can produce steam to operate **turbines** coupled with alternators to produce electricity. **1.4 – ITER** 



ITER site, September 2023. ©ITER



ITER Tokamak and Plant Systems, 2016. ©ITER

**Members:** China, the European Union (through Euratom), India, Japan, Korea, Russia and the United States.

#### Main goals:

- **DT plasma** with fusion mostly sustained by **internal fusion heating**.
- **500 MW fusion power** in long plasma pulses with **Q=10** (ten-fold return on power by the plasma: 500 MW of fusion power from 50 MW of input heating power)
- Contribute to the demonstration of the integrated operation of technologies for a fusion power plant.
- Test tritium breeding.
- Demonstrate the **safety characteristics** of a fusion device.

#### More information: ITER - the way to new energy

#### 1.5 – Currently operated and future tokamaks in EU or with EU participation





MAST Upgrade, UK











#### **1.6 – Very different sizes and shapes**



#### **1.7 – JET, the tokamak designed to study fusion power**



#### **1.8 – Other tokamaks in the world**



Fusion World (remdelaportemathurin.github.io)

#### 1.9 – All these machine pave the way to ITER !

Ignition condition (Lawson criteria): n  $T_i \tau_E > 10^{21} \text{ keV m}^{-3} \text{ s}$ 



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#### 2 – Electronics in tokamaks

# 2.1 - Which electronics?

- Analogue and digital electronics
  - Control of auxiliary systems

Vacuum, heating, cooling, magnets, fueling, ...

- Control and readout of diagnostics

Magnets, neutrons, optical viewing, bolometry, spectroscopy, microwaves, plasma operation, ...

## 2.2 - Integration

• Embedded in systems

Large quantity and diversity

• Housed in cabinets

Several hundreds of cabinets,

Large quantity and diversity of electronic systems.

Almost all COTS (Commercial Off The Shelf)

Several hundreds of Gbits SRAM

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#### 3 – Radiation in tokamaks

**3.1 - Main radiation sources <u>during DT plasma operation</u>:** 

#### 3.1.1 - Plasma neutrons

DT nuclear reactions in the plasma produce 14.1 MeV neutrons

<sup>2</sup>D + <sup>3</sup>T → <sup>4</sup>He (3.5 MeV) + n (14.1 MeV)



# 3.1.2 - Prompt gammas generated by plasma neutrons The de-excitations of excited nuclei following nuclear interactions between a plasma neutron and the nuclei of an atom of the tokamak structure, generate prompt gamma.



### 3 – Radiation in tokamaks

#### 3.1 - Main radiation sources during DT plasma operation

#### 3.1.3 - Neutrons and photons generated by activated water

High energy neutrons from DT plasma fusion reaction interact with oxygen isotopes <sup>16</sup>O and <sup>17</sup>O from tokamak cooling water, which creates short live isotopes <sup>16</sup>N and <sup>17</sup>N,

which then undergoes beta decay producing gammas and neutrons.

•  ${}^{16}O + n$  (12.2 MeV threshold)  $\rightarrow p + {}^{16}N$ 

β decay: <sup>16</sup>N (T<sub>1/2</sub> = 7.13 s) → <sup>16</sup>N +  $\gamma$  (69% @ 6.13 MeV, 5% @ 7.12 MeV)

<sup>17</sup>O + n (8.4 MeV threshold) → p + <sup>17</sup>N
β<sup>-</sup>n decay: <sup>17</sup>N (T<sub>1/2</sub> = 4.17 s) → <sup>17</sup>O\* → <sup>16</sup>O + n (0.901 MeV) + γ (0.871 MeV)

<sup>16</sup>N and <sup>17</sup>N decay times are sufficiently long for water to travel far enough from the activation source (plasma) to cause the presence of activated water all along the cooling water pipework.





**3.2** - Main radiation sources *during maintenance and shutdowns* following DT plasma operation:

# 3.2.1 - $\gamma$ emitted by activated materials

- Gammas from cooling water pipework activated by neutrons from activated water.
- Gammas from tokamak structural elements (vacuum vessel and its content, cryostat, ...) activated by plasma neutrons.



### 3 – Radiation environment in tokamaks

#### 3.3 - Radiation environment impacting electronics reliability:

- Total Ionizing Dose (TID)
   Accumulated dose causes accumulated degradation due to accumulated charges trapping in oxides and at oxide-semiconductor interface.
- 1 MeV Equivalent Neutron Fluence (ENF)
   Accumulated fluence causes accumulated degradation
   due to accumulated displacement damages in the semiconductor lattice.
- Total Neutron Flux (TNF) with its specific energy spectrum
   An individual neutron causes an instantaneous single event effect (SEE)
   due to instantaneous charges deposition in sensitive nodes of semiconductor devices.

#### 3.4 - Calculation of the radiation environment in a tokamak building

- **3D digital Monte-Carlo simulation** using tools such as Geant4, MCNP, etc.
- These simulations take into account :
  - the radiation sources (plasma, activated water, ...),
  - the status of the tokamak (DD or DT plasma operation, shutdown/maintenance),
  - $\circ$   $\,$  the shielding provided by the building structure.

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#### 4 – Neutron interactions with atoms of semiconductor components

4.1 - Mechanisms of interaction of incident neutrons with the atoms of the electronic component



#### Notation, examples:

- ${}^{28}Si(n,p){}^{27}Al$  represents the charged absorption reaction  $n + {}^{28}Si \rightarrow p + {}^{27}Al$
- ${}^{10}B(n,\alpha)^7$ Li represents the charged absorption reaction  $n_{th} + {}^{10}B \rightarrow {}^{4}He (1.47 \text{ MeV}) + {}^{7}Li (0.84 \text{ MeV}) + \gamma (0.48 \text{ MeV})$
- ${}^{28}Si(n,n){}^{28}Si$  represents the elastic scattering reaction  $n + {}^{28}Si \rightarrow n + {}^{28}Si$

Figure: J.L. Autran, D. Munteanu, "Soft Errors, from Particles to Circuits", CRC Press Adapted from Rinard P. Neutron interations with matter. Los Alamos Technical Report. Available at: <u>http://www.fas.org/sgp/othergov/doe/lanl/lib-www/la-pubs/00326407.pdf</u>)

#### 4 – Neutron interactions with atoms of semiconductor components 4.2 –Interaction of an incident neutron with an atom of a semiconductor device

There are many possible interactions. The probability of a given interaction depends on:

- The neutron flux and energy (or energy spectrum)
- The composition of the semiconductor device (3D density of constituting atoms)



•Below  $\sim 5 MeV$ , only elastic scattering and inelastic scattering => only target nuclei recoil.

Above ~5 *MeV*, non-elastic absorption also occur (producing secondary fragments) => the % of elastic + inelastic decreases.
At 14 MeV, ~70 % of elastic + inelastic (only target nuclei recoil), and ~30% non-elastic absorption (secondary fragments).

Pictures: Courtesy J.L.Autran, Rennes University / Institut de Physique de Rennes

#### 4 – Neutron interactions with atoms of semiconductor components 4.3 - Interaction of incident neutrons with <sup>10</sup>B

- In integrated circuits, the interconnections are frequently separated by an insulator made of **borophosilicate glass** (BPSG) whose natural boron contains 20% <sup>10</sup>B.
- Natural boron with 20% <sup>10</sup>B is also present in other places in integrated circuits, e.g. in tungsten plugs.
- The collision cross section of thermal neutrons with <sup>10</sup>B decreases with energy as  $1/\sqrt{E}$ .





- 4 Neutron interactions with atoms of semiconductor components 4.4 – Energy deposition of a recoil particle resulting from neutron-atom interaction
- Nuclear stopping power = loss of energy due to collisions of recoil atom with nuclei of the target material => Cascade of collisions => shower of recoil particles, until their final stop.



Electronic stopping power = loss of energy due to interactions of recoil atom with electrons
of atoms of target material => lonization => high electron-hole pairs density along the track.

The electronic stopping power is also called LET (Linear Energy Transfer).

Weighted LET =  $-\frac{1}{\rho} \frac{\Delta E}{\Delta x}$  (MeV.cm<sup>2</sup>/mg)



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#### 4 – Neutron interactions with atoms of semiconductor components 4.4 – Energy deposition of a recoil particle resulting from neutron-atom interaction

The key parameters that control single event effects in semiconductor devices are the LET and the range of the recoil particle, which depend on the recoil particle and on its energy.

The nature and the energy of the recoil particle depend on the neutron interaction mechanism, which depend on the target material and on the energy of the incident neutron.



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## 5.1 - Accumulated damages caused by Total Ionizing Dose (TID) :

- TID Unit: Gray (Gy). 1 Gy = 1J/kg = 100 rads
- Cumulative effect.
- Effects: Power consumption increase, parametric characteristics shifts, permanent failure.
- No effects reported below 1 Gy.

## 5.2 - Accumulated damages caused by 1 MeV Equivalent Neutron Fluence (ENF)

• ENF Unit: 1 MeV equivalent neutron fluence (n/cm<sup>2</sup>)

1 MeV neutron fluence producing the same displacement damage than the fluence of the considered neutron environment.

- Impacts diodes and BJTs. No impact on MOS devices.
- Cumulative effect.
- Parametric characteristics shifts, then permanent failure.
- No effects reported below 10 n/cm<sup>2</sup>.





# 5.3.1 - Instantaneous effects caused by Total Neutron Flux (TNF)

- TNF Unit: n/cm<sup>2</sup>/s
- Individual neutrons causes instantaneous Single Event Effects (SEE), such as e.g.:

Designation	Effects	Correction
Single Event Upset (SEU)	bit-flips in memories, causing	Reboot (rewriting memories)
Single Event Latch-up (SEL)	short circuits in parasitic thyristors, causing overconsumption, non-functioning, or destruction	OFF/ON power cycling replacement
Single Event Burnout (SEB)	destruction	replacement
Single Event Gate Rupture (SEGR)	destruction	replacement

- SEUs can cause a single bit upset (SBU), or multiple bit upsets (MBU-2, MBU-3, ... MBU-10), depending on the LET and range of the recoil particle.
- SEUs can have a delayed effect : a bit-flip may cause a silent data error which causes an abnormal operation or a failure <u>only when the corrupted bit is used by the electronic function</u>.

# 5.3.2 - Neutron-induced SEEs are based on an *indirect* mechanism

• An incident neutron interacts with a nucleus of the material of the electronic component, producing a recoil *ionized* particle.

#### Example:

 $n_{th} + {}^{10}B \rightarrow {}^{4}He (1.47 \text{ MeV}) + {}^{7}Li (0.84 \text{ MeV}) + \gamma (0.48 \text{ MeV})$ 

- The recoil ionized particle causes an ionization along its track inside the semiconductor material.
- The ionized track which crosses a sensitive node (e.g. a reversebiased p-n junction) of a semiconductor device.
- The sensitive node collects a large fraction of the charges of the track (e.g. thanks to the electrical field of the reverse-biased junction).
- The collected charges causes a SEE (see examples in backup slides).



5.4 – Neutron-induced single event rate (SER) model:

• 
$$SER = \int \sigma^{SEE}(E) \times \frac{d\phi(E)}{dE} dE$$

[s<sup>-1</sup>] (or s<sup>-1</sup>/bit for memories)

•  $\sigma^{SEE}(E)$  = effective neutron-SEE cross-section

[cm<sup>2</sup>] (or cm<sup>2</sup>/bit for memories)

•  $\frac{d\phi(E)}{dE}$  = differential neutron flux

• *E* = neutron energy

[cm<sup>-2</sup>.s<sup>-1</sup>.MeV<sup>-1</sup>]

Tokamaks : thermal to 14 MeV

# 5.4 – Neutron-induced single event rate (SER) model:

 $\sigma^{SEE}(E)$  depends on the energy of the recoil particle, which depends on the neutron-induced SEE mechanism, the energy of the incident neutron, and the struck atom.  $\sigma^{SEE}(E) = \sigma^{SEE}_{ThN}(E) + \sigma^{SEE}_{FN}(E)$ .

Thermal neutrons SEE cross-section



Fast neutrons SEE cross-section :

$$\sigma_{FN}^{SEE}(E) = \sigma_{sat}^{SEE} \left( 1 - e^{-\left(\frac{E - E_{th}}{w}\right)^{s}} \right)$$

Weibull function's 4 parameters:

- $\sigma_{sat}^{SEE}$  = saturation cross section,
- $E_{th}$  = threshold below which  $\sigma_{FN}^{SEE}(E) = 0$ ,
- w = scale parameter,
- **s** = shape parameter.

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## 6.1 – Background

## 6.1.1 - Standards for nuclear electronics:

 IEC-61508 "Functional safety of electrical/electronic/programmable electronic safety related systems".

 $\Rightarrow$  Requires a reliability demonstration for critical non-safety electronics.

IEC-61511 "Nuclear Power Plants – I&C Important to Safety – General requirements for systems".
 ⇒ Requires a reliability demonstration for safety electronics.

#### 6.1.2 - Reliability units

- **FIT** = Failures in Time (1 FIT = 1 failure / billion hour)
- MTTF = Mean Time to Failure
- MTBF = Mean Time between Failures.

## 6.1.3 - Reliability defined by manufacturer

- Valid in natural terrestrial ground level environment
- No longer valid in an artificial environment more demanding than the terrestrial one.

#### **6.1.4 - Radiation qualification of COTS electronics is not possible**, for the following reasons:

- The radiation tolerance of non-rad-hard semiconductor devices is fortuitous and varies from batch to batch.
- COTS electronic systems have no traceability of origin of manufacturing batches of constituting semiconductor components
  - ⇒ Radiation test results on a specimen are not valid for another identical specimen (same part number).

#### 6.1.5 - Developing radiation-hard electronics for tokamaks is not realistically doable.

• Too complex, too large quantities, too large diversity, too high cost, ....

Alternative approach to demonstrate reliability in the artificial radiation environment : Replace the *qualification of electronics for its radiation environment* (not doable) by the *qualification of the radiation environment for general electronics*.

#### 6.2 – How to ensure electronics reliability in tokamaks radiation environment

#### 6.2.1 - Critical and safety electronics

- Must be installed in shielded areas ensuring radiation conditions demonstrated equivalent, in terms of impact on the reliability of general modern electronics, to the natural terrestrial environment, in which the reliability established by the manufacturer is valid.
  - TID ≤ 1Gy and ENF ≤ 10 n/cm<sup>2</sup> can be considered as harmless (no reliability impact on electronics reported in any compendium of radiation test results).
  - If TID or ENF during expected life cycle are above these values (e.g. up to x4), periodical replacement could be a solution (if doable).
  - TNF must ensure RDF ≤ 1 for general modern electronics.

RDF (Reliability Degradation Factor) =  $\frac{SER \text{ in artificial radiation shielded environment}}{SER \text{ in natural radiation terrestrial environment}}$ 

#### 6.2.2 – Non-critical non-safety electronics

- The above requirements can be relaxed (degraded reliability acceptable).
- But they cannot be fully cancelled (such electronics must work, otherwise no need to install it).







- 6 How to ensure electronics reliability in tokamaks radiation environment
- 6.4 Determination of worst-case RDF for general modern electronics in a given artificial environment
  - A Determination of parameters of  $\sigma_{ThN}^{SEE}(E)$  and  $\sigma_{FN}^{SEE}(E)$  models for general modern electronics by SEE measurements on a panel of components representative of general modern electronics **Constituents of the panel:** 
    - Only semiconductor <u>components</u> (no electronic <u>systems</u>), the most basic possible, known to be sensitive to neutron-induced SEEs,
    - Several device types (memories, power devices, ...),
    - Several technologies,
    - Several technology nodes,
    - Several manufacturers.

# **B – Determination of the worst-case RDF at electronics location in the tokamak** for general electronics

- Calculation of RDF at electronics location for each component of the panel
- Determination of the worst-case RDF (RDF<sub>GE</sub>), deemed to be representative of general electronics (GE).

6.5 – Shielding improvement to ensure electronics reliability in a tokamak radiation environment

6.5.1 – Targets

• For critical/safety electronics:

 $\frac{\mathsf{RDF}_{\mathsf{GE}} \leq 1}{\mathsf{TID} \leq 1 \text{ Gy}}$  $\mathsf{ENF} \leq 10 \text{ n/cm}^2$ 

• For other electronics, determine acceptable values, e.g.

 $RDF_{GE} \le 10-100$ TID  $\le 10-50$  Gy ENF  $\le 100-500$  n/cm<sup>2</sup>

- 6 How to ensure electronics reliability in tokamaks radiation environment
- 6.5 Shielding improvement to ensure electronics reliability in a tokamak radiation environment

6.5.2 – Shielding incorporated in building structure (e.g. shielded walls, shielded slabs)

- Against gamma photons,
- Against fast neutrons.

Not recommended against <u>thermal</u> neutrons because the remaining fast neutrons will thermalize at electronics location.

#### 6.5.3 – Local shielding on electronics cabinets

- Recommended against <u>thermal</u> neutrons (light, thin B<sub>4</sub>C panels requires careful 3D design and digital Monte-Carlo simulation to optimize shielding of openings for heat removal and of cables penetrations).
- Not recommended against photons or fast neutrons (would require steel/concrete: too bulky, too heavy, limited effectiveness).

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#### 7 – Validation of SER prediction method & model under DD and DT plasma neutrons

To illustrate the above section 6, this section 7 presents a preliminary study currently being conducted by CEA-IRFM, CERN, Aix-Marseille University and University of Rennes in collaboration with STMicroelectronics, aiming at replacing the radiation qualification of the electronics (not doable) by the qualification of the radiation environment at electronics location in tokamaks and in particle accelerators.

# 7.1 – Validation at WEST tokamak (CEA/IRFM) under D-D plasma neutrons

#### Real Time Soft Error Rate test bench from AMU/IM2NP:

"Preliminary Study of Electronics Reliability in ITER Neutron Environment

/ DOI:10.1109/RADECS55911.2022.10412483]

/ IEEE /

proceedings ,

[M. Dentan et Al., RADECS 2022 pro 384 memory chips (65 nm bulk SRAM from STM, BPSG-free, 3,226 Gbits in total.

**DIAMON neutron spectrometer from Raylab :** energy range = from thermal to 20 MeV.



#### 7.2 – Validation at JET tokamak (EUROfusion and UKAEA) under D-T plasma neutrons



[M. Dentan et Al., "Real-Time SER Measurements of CMOS bulk 40 nm and 65 nm SRAMs Combined with Neutron Spectroscopy at the JET Tokamak during D-D and D-T Plasma Operation", to be published in IEEE TNS / Proceedings of NSREC 2024 Conference]



# The results of the preliminary study conducted at WEST then at JET validate the SER prediction methods and models.

#### Next steps:

- Determination of parameters of  $\sigma_{ThN}^{SEE}(E)$  and  $\sigma_{FN}^{SEE}(E)$  models for general modern electronics by SEE measurements on a panel of components representative of general modern electronics.
- Determination of the worst-case RDF<sub>GE</sub> at electronics location in the artificial environment of a tokamak and of an accelerator.
- Shielding improvement to ensure electronics reliability in the artificial radiation environment of tokamaks and accelerators (worst-case RDF<sub>GE</sub> ≤1 for critical electronics)



# cea Back-up slides

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5.2.1 - Example of neutron-induced SEE : SEU (bit-flip) in 6 MOSFET SRAM memory cell



[S. Gerardin "Corso di qualita e affidabilita pin elettronica: Single Event Effects«, 2015-2016]

When Wordline = '0', no access to transistors => the cell is holding its stored data using the back-to-back inverter configuration.

- (a) Initially, node  $\overline{Q} = 0^{\prime}$  and node  $Q = 1^{\prime} (+Vdd)$ .
- (b) an ionized particle crosses the drain of transistor  $M_3$ .
- (c) Negative charges are collected on the drain of  $M_3 => a$  transient current propagates through the inverter and changes the state of node  $\overline{Q}$  from '0' to '1'. This change propagates backwards through the inverter and changes of state of node Q from '1' to '0' => the two nodes have flipped ( $\overline{Q} = '1'$  and Q = '0'), and the memory cell now stores a false value.

#### 5.2.2 - Example of neutron-induced SEE : single event latch-up (SEL) in the parasitic thyristor of CMOS pair



- In normal operation, the parasitic thyristor in OFF (the parasitic transistors Qn and Qp are both OFF)

- An individual recoil particle from neutron interaction creates electron-hole pairs along its track.
- This ionized track crosses a sensitive node of Qn, which puts it in ON mode. This conductive mode of transistor Qn is propagated to transistor Qp through the parasitic thyristor structure, and transistor Qp goes to conductive mode, which stabilizes the conductive mode of transistor Qn.
- With both Qn and Qp in conductive mode, the thyristor is in conductive mode. This conductive mode is stable and remains as long as the inverter is supplied with power.

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N+

#### 5.2.3 - Example of neutron-induced SEE : single event gate rupture (SEGR) in a power MOSFET structure







- (a) Schematic view of power MOSFET cross section.
- (b) ON mode. Current flows from source to drain. No electrical field in N/N+ drain junction.
- (c) OFF mode. An ionized track crosses the structure. High electrical field in N/N+ drain junction separates its (+) and (-) charges.
- (d) The (+) charges accumulate below gate oxide, and image (-) charges accumulate above gate oxide.
- (e) This causes a high electrical field through the gate oxide, which causes a gate rupture.