INSTITUT LAUE LANGEVIN

Thermal neutron detector principles, front end electronics and signal processing

ILL SCI group

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THE EUROPEAN NEUTRON SOURCE

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- Thermal neutrons
 - Have almost no kinetic energy
 - Are neutral
 - => They are essentially « invisible »

 In order to detect them, we need a nuclear reaction with energy liberation



- Reaction n(He-3,p)H-3 + 0.77 MeV (300 ppm)
- Reaction n(Li-6,alpha)H-3 + 4.79 MeV (7.8%)
- Reaction n(B-10,alpha)Li-7 (+ gamma) + 2.79 MeV (20%)
- Reaction n(U-235, fission) + ~100 MeV (0.7%)
- Reaction n(Gd-157,Gd) e- 0.182 MeV (15.7%)



- Heavy ions after conversion (except Gd-157)
 - Ionisation in gas (track ~mm)
 - Excitation in scintillator (track ~micrometer)
 - Injection from layer or bulk in semiconductor
- Detecting secondary signal (charge or light)



- Neutron beams usually have low flux density
 - Need for relatively wide beams and large samples
 - Angular resolution needs large distances
- Neutron detectors usually are
 - Big in size
 - Do not need very fine spatial resolution
- But there are exceptions...













Charge division : theoretical noise







Charge division : theoretical spatial resolution

$$\delta_{rms} D = \sqrt{\int_{f=0}^{\infty} |H|^2 S_{min} df} \qquad \delta_{rms} S = \sqrt{\int_{f=0}^{\infty} |H|^2 S_{plus} df}$$



Position resolution (1 sigma)

Time scaling and amplifier gain

A « unit charge » will generate an impulse response of which the height is the « gain » of the amplifier chain.

Scaling the time axis (k times faster), but keeping the gain (maximum value):





Time scaling and S/N ratio





Time scaling : S/N ratio (2)

- When k times faster, S/N ratio sqrt(k) times better
- Smallest band width lowest noise
- Shortest pulse in time lowest dead time
- => Gaussian shape
 - Is the mathematical curve with narrowest time domain and frequency domain extension



Analogue gaussian shaping







Analogue gaussian shaping 2











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Overall semi-analogue readout structure



FOR SOCIET'

- CAEN digitizer board V1740 (2007 creation)
 - 64 12-bit ADC channels @ 62.5 MHz
 - Altera Cyclone III FPGA (1 FPGA per 16 channels)
 - We asked CAEN to upgrade the Cyclone to the biggest pin-pin compatible FPGA that could be integrated in the board
 - EP3C40F484
 - 39600 LE / 126 multipliers / 126 M9K memory blocks
- Agreement to open up firmware for pulse processing



- Very limited ressources to
 - Implement base line correction
 - Implement pole-zero correction
 - Implement 2 sections of Gaussian filter
 - Implement linear position calibration
- Hence
 - Use IIR implementations
 - « outsource » divisions



IIR implementation via impulse invariance method:

$$H(s) = \frac{1}{s - s_0} \qquad h(t) = e^{s_0 t} \qquad \qquad h_z(n) = e^{s_0 n T_z} = T_s h(n T_s) = T_s (e^{s T_s})^n \\ H_z(z) = \frac{T_s}{1 - e^{s_0 T_s} z^{-1}}$$

$$H(s) = \sum_{i} \frac{A_{i}}{s-s_{i}} \text{ gives } h(t) = \sum_{i} A_{i} e^{s_{i}t} \text{ so } h_{z}(n) = T_{s}h(nT_{s}) = T_{s}\sum_{i} A_{i} e^{s_{0}nT_{s}}$$

$$H_{z}(z) = T_{s} \sum_{i} \frac{A_{i}}{1 - e^{s_{i}T_{s}} z^{-1}} \qquad s_{i} \rightarrow e^{s_{i}T_{s}}$$



Our analogue (normalized with tau = 2 pi) Gaussian filter approximation:

-1.35536 +/- I 0.32795 Has 4 poles -1.18108 +/- I 1.0604

$$\frac{1}{(1-a_1z^{-1}+b_1z^{-2})(1-a_2z^{-1}+b_2z^{-2})}$$
$$u[n]=i[n]+a_ku[n-1]-b_ku[n-2]$$

 $\frac{4.899}{4.899+11.42\,s+10.87\,s^2+5.073\,s^3+s^4}$

$$a_1 = 2e^{-1.355 T} \cos(0.3279 T)$$

$$b_1 = e^{-2.711 T}$$

$$a_2 = 2e^{-1.181 T} \cos(1.0604 T)$$

$$b_2 = e^{-2.362 T}$$



H(s) =

Pole zero compensation:

$$H(s) = \frac{s + 1/\tau_l}{s + 1/\tau_s} = 1 + \frac{1/\tau_l - 1/\tau_s}{s + 1/\tau_s}$$

$$Z(z) = 1 + (T_s/\tau_l - T_s/\tau_s) \frac{1}{1 - e^{-T_s/\tau_s} z^{-1}}$$

 $e^{-T_s/\tau_s} \approx 1 - \frac{T_s}{\tau_s}$

$$w[n] = i[n] + (1 - \frac{T_s}{\tau_s})w[n-1] \text{ and}$$
$$u[n] = i[n] + (T_s/\tau_l - T_s/\tau_s)w[n]$$

Digital implementation of a channel



The channel signal treatment bloc consists of 5 elements:

- 1. A first baseline correction circuit, applied to the incoming signal
- 2. A pole-zero compensation circuit
- 3. A first second-order gaussian filter section
- 4. A second second-order gaussian filter section
- 5. A final baseline correction circuit



Deployment at the ILL

- D11, D22, D33
- Figaro, D17
- IN5, Panther, Sharp
- Wasp, IN12, D2B







