

#### EPFL-ACH Pr. Paolo Ricci (Academic Director & PI) Dr. Gilles Fourestey (Operations Director)



## **EPFL-ACH in a nutshell**



Support center for HPC applications and provider of advanced computing platforms (~30 people)

Experiment a Museology

Virtual, augmented, mixed reality, through advanced computer science and state-of-the-art visualization facilities (~10 people)



Computational Science and Engineering Mathematics group (~70 people)



C) EUROfusion

Swiss Data Science Center, national institute for artificial intelligence and machine learning techniques (~50 people)

(~ 200 people, Swiss theory group: Plasma Center ~ 40 people)





# A comprehensive support, from HPC code design to visualization

We are a competence center for

- methods, providing specific support to specific needs
- applications, developing and maintaining EUROfusion software



# EPFL An attr





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## ... to an even larger involvement



## **EPFL** Large voluntary contribution

![](_page_5_Figure_1.jpeg)

![](_page_6_Picture_0.jpeg)

# GPU porting of ASCOT5 code for Monte Carlo simulations in fusion plasmas

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#### HPC ACH F2F Meeting

![](_page_6_Picture_4.jpeg)

## EPFL ASCOT5

![](_page_7_Picture_1.jpeg)

![](_page_7_Picture_2.jpeg)

- ASCOT5 is a test **particle orbit-following** code for toroidal magnetically confined fusion devices
- The code uses the Monte Carlo method to solve the distribution of particles by following their trajectories.
  - The evolution of the distribution function for a test particle species *a* is described by the Fokker-Planck equation  $\partial f_a = \nabla f_a + \frac{q_a}{r} (\mathbf{E} + \mathbf{x} \times \mathbf{B}) \nabla f_a = \sum \nabla \nabla f_a = \sum \nabla \nabla f_a + \nabla f_a$

$$\frac{\partial f_a}{\partial t} + \mathbf{v} \cdot \nabla f_a + \frac{q_a}{m_a} (\mathbf{E} + \mathbf{v} \times \mathbf{B}) \cdot \nabla_{\mathbf{v}} f_a = \sum_b -\nabla_{\mathbf{v}} \cdot [\mathbf{a}_{ab} f_a - \nabla_{\mathbf{v}} \cdot (\mathbf{D}_{ab} f_a)]$$

and **approximated by the Langevin equation** for a large number of markers that represent the distributed function:

 $d\mathbf{z} = [\dot{\mathbf{z}} + \mathbf{a}(\mathbf{z}, t)] dt + \boldsymbol{\sigma}(\mathbf{z}, t) \cdot d\boldsymbol{\mathcal{W}}$ 

- The particles undergo collisions with a static Maxwellian background plasma
- The detailed magnetic fields and the first wall can be fully 3D
- MPI, TLP (OpenMP, task-based), DLP (SIMD)

![](_page_7_Figure_12.jpeg)

![](_page_8_Picture_0.jpeg)

![](_page_8_Picture_2.jpeg)

#### MPI - OpenMP - SIMD implementation:

- The time evolutions of each particle are independent from each other, particles having different lifetimes
- One + two levels of parallelism:
  - MPI: Particles distributed among tasks, fields replicated
  - OpenMP: queue based approach
  - SIMD: each lane handles a particule during its lifetime (events) independently
- swapping mechanism
  - after each iteration, particles that have reached their end condition are stored in an array for completed particles
  - a fresh particle is retrieved from a queue to continue simulation in the particular slot in the N<sub>SIMD</sub> arrays

![](_page_8_Picture_12.jpeg)

#### Algorithm 1: CPU multithread vectorized algorithm

initialization: #pragma omp parallel while particles are alive in pack<sub>Neump</sub> do #pragma omp simd for particles  $\in pack_{N_{SIMD}}$  do move\_particle; end #pragma omp simd for particles  $\in pack_{NSIMD}$  do collisions; end #pragma omp simd for particles  $\in pack_{N_{SIMD}}$  do end condition; end #pragma omp simd for particles  $\in pack_{N_{SIMD}}$  do diagnostics; end for particles  $\in pack_{N_{SIMD}}$  do if particle reached end condition then store particle and replace it by new one end end end

![](_page_9_Picture_0.jpeg)

![](_page_9_Picture_2.jpeg)

![](_page_9_Picture_3.jpeg)

- First implementation History-Based:
  - parallelism is expressed at a high level, emphasizing the independence of individual particles
  - each GPU thread deals with the entire history of one or more particles until all of the particles have reached their end condition
  - this parallelism is implemented through a single monolithic GPU kernel

| Algorithm 2: GPU algorithm - History-based |  |
|--|--|
| initialization;                            |  |
| #pragma acc parallel loop                  |  |
| for all particles $\in \{1N_{tot}\}$ do    |  |
| while particle is alive do                 |  |
| move_particle;                             |  |
| collisions;                                |  |
| end_condition;                             |  |
| diagnostics;                               |  |
| end  |  |
| end  |  |

| Results:                              | ASCOT5         | TTS [s]           | may2022_2dwall_go_analyticB |        |                   |                 |
|---------------------------------------|----------------|-------------------|-----------------------------|--------|-------------------|-----------------|
| May2022 Benchmark Comparison          |                | markers:          | 10000                       | 100000 |                   |                 |
|                                       |                |                   |                             |        | Platform          | Compiler        |
| GPU and CPU versions have similar TTS | m100@CINECA    | OpenMP<br>Offload | 46                          | 473    | Power9 + v100     | XL compilers    |
| (in general)                          | Phoenix@EPFL   | OpenMP<br>Offload | 232                         | 2143   | 6138 gold + v100  | gcc 11          |
|                                       | Phoenix@EPFL   | OpenACC           | 48                          | 261    | 6138 fold + v100  | gcc 11          |
|                                       | Helvetios@EPFL | OpenMP            | 87                          | 860    | 2x Gold 6140      | intel compilers |
|                                       | Jed@EPFL       | OpenMP            | 31                          | 318    | 2x Platinum 8360Y | intel compilers |

![](_page_10_Picture_0.jpeg)

![](_page_10_Picture_2.jpeg)

- GPU porting strategy
  - Maintain a single version of the code
  - Ensure code portability and readability
  - Generic pragma for OpenMP/OpenACC

```
#ifndef gpu commands
#define gpu commands
/ * *
 * @brief Applies parallel execution to loops
#if defined(GPU) && defined(OPENMP)
#define GPU PARALLEL LOOP ALL LEVELS\
         str_pragma (omp target teams distribute parallel for sim)d
#elif defined(GPU) && defined(OPENACC)
#define GPU PARALLEL LOOP ALL LEVELSstr pragma (acc parallel loop)
#else
#define GPU PARALLEL LOOP ALL LEVELSstr pragma (omp simd)
#endif
 * @brief Maps variables to the target device
 * /
#if defined(GPU) && defined(OPENMP)
#define GPU MAP TO DEVICE(...) \
         str pragma (omp target enter data map (to: VA ARGS ))
#elif defined(GPU) && defined(OPENACC)
#define GPU_MAP_TO_DEVICE(...) str pragma acc enter data copyin
( VA ARGS ))
#else
#define GPU MAP TO DEVICE(...)
#endif
. . . . . . . . . . . .
#endif
#endif
```

```
GPU_LOOP_ALL_LEVELS
for(i = 0; i < n_queue_size; i++) {
    if(p->running[i]) {
        posxyz[0] = posxyz0[0] + pxyz[0] * h[i] / (2.0 * gamma * mass);
        posxyz[1] = posxyz0[1] + pxyz[1] * h[i] / (2.0 * gamma * mass);
        posxyz[2] = posxyz0[2] + pxyz[2] * h[i] / (2.0 * gamma * mass);
    }
GPU_END_LOOP_ALL_LEVELS
```

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![](_page_11_Picture_2.jpeg)

- The original implementation is not GPU-friendly:
  - one very large kernel (1000+ threads/kernel)
  - events depend on the previous event

#### Implement a new version by splitting the initial kernel:

- Parallelize over events instead of particles
- small kernels independent of each other

#### SUCCESSFUL VECTORIZATION - REACTOR PHYSICS MONTE CARLO CODE

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Most particle transport Monte Carlo codes in use today are based on the "history-based" algorithm, wherein one particle history at a time is simulated. Unfortunately, the "history-based" approach (present in all Monte Carlo codes until recent years) is inherently scalar and cannot be vectorized. In particular, the history-based algorithm cannot take advantage of vector architectures, which characterize the largest and fastest computers at the current time, vector supercomputers such as the Cray X/MP or IBM 3090/600. However, substantial progress has been made in recent years in developing and implementing a vectorized Monte Carlo algorithm. This algorithm follows portions of many particle histories at the same time and forms the basis for all successful vectorized Monte Carlo codes that are in use today. This paper describes the basic vectorized algorithm along with descriptions of several variations that have been developed by different researchers for specific applications. These applications have been mainly in the areas of neutron transport in nuclear reactor and shielding analysis and photon transport in fusion plasmas. The relative merits of the various approach schemes will be discussed and the present status of known vectorization efforts will be summarized along with available timing results, including results from the successful vectorization of 3-D general geometry, continuous energy Monte Carlo.

![](_page_11_Picture_13.jpeg)

Algorithm 3: GPU algorithm - Event-based

initialization; while number of particles alive > 0 do #pragma acc parallel loop for all particles  $\in \{1...N_{tot}\}$  do if particle alive then move particle; end end #pragma acc parallel loop for all particles  $\in \{1...N_{tot}\}$  do if particle alive then collisions: end end #pragma acc parallel loop for all particles  $\in \{1...N_{tot}\}$  do if particle alive then end condition; end end #pragma acc parallel loop for all particles  $\in \{1...N_{tot}\}$  do if particle alive then diagnostics; end end end

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![](_page_12_Picture_2.jpeg)

![](_page_12_Picture_3.jpeg)

- parallelize over events instead of particles
- small kernels independent of each other
- **pack particles** to avoid thread divergence and unbalance

end

end

| Algorithm 2: GPU algorithm - History-based   | Algorithm 3: GPU algorithm - Event-based   | Algorithm 4: GPU algorithm - Event-based - packing   |
|--|--|--|
| initialization;<br>#pragma acc parallel loop<br>for all particles ∈ {1N <sub>tot</sub> } do<br>while particle is alive do<br>move_particle;<br>collisions;<br>end_condition;<br>diagnostics;<br>end<br>end | initialization;<br>while number of particles alive > 0 do<br>#pragma acc parallel loop<br>for all particles $\in \{1N_{tot}\}$ do<br>if particle alive then<br>move_particle;<br>end<br>end<br>#pragma acc parallel loop<br>for all particles $\in \{1N_{tot}\}$ do<br>if particle alive then<br>collisions;<br>end<br>end<br>#pragma acc parallel loop<br>for all particles $\in \{1N_{tot}\}$ do<br>if particle alive then<br>end<br>end<br>#pragma acc parallel loop<br>for all particles $\in \{1N_{tot}\}$ do<br>if particle alive then<br>end<br>end<br>#pragma acc parallel loop<br>for all particles $\in \{1N_{tot}\}$ do<br>if particle alive then<br>dignostics;<br>end | $\begin{array}{l} \text{initialization;}\\ N_{pack} \leftarrow N_{tot};\\ \text{while number of particles alive > 0 do}\\ & \texttt{#pragma acc parallel loop}\\ & \texttt{for } packed particles still alive \in \{1N_{pack}\} \text{ do}\\ & \mid \text{ move_particle;}\\ & \texttt{end}\\ & \texttt{#pragma acc parallel loop}\\ & \texttt{for } packed particles still alive \in \{1N_{pack}\} \text{ do}\\ & \mid \text{ collisions;}\\ & \texttt{end}\\ & \texttt{#pragma acc parallel loop}\\ & \texttt{for } packed particles still alive \in \{1N_{pack}\} \text{ do}\\ & \mid \text{ collisions;}\\ & \texttt{end}\\ & \texttt{#pragma acc parallel loop}\\ & \texttt{for } packed particles still alive \in \{1N_{pack}\} \text{ do}\\ & \mid \text{ end_condition;}\\ & \texttt{end}\\ & \texttt{#pragma acc parallel loop}\\ & \texttt{for } packed particles still alive \in \{1N_{pack}\} \text{ do}\\ & \mid \text{ diagnostics;}\\ & \texttt{end}\\ & \texttt{if } (N_{pack} - N_{running} > \alpha \cdot N_{tot}) \texttt{ then}\\ & \mid \text{ pack particles;}\\ & N_{pack} \leftarrow N_{running};\\ & \texttt{end}\\ & \texttt{end} \end{array}$ |
|  | ena  |  |

### EPFL Benchmarks

![](_page_13_Picture_1.jpeg)

- Benchmark:
  - Collisional full-orbit simulation of prompt-losses of fusion alpha particles
  - o 2D wall; ITER-like but circular equilibrium interpolated with cubic splines
  - 2D wall rectangular, coulomb collisions, gyro orbit, simulation time = 0.0001s, fixed time step
  - Leonardo: A100, nvhpc/23.1
  - Comparison of three GPU implementations on GPU A100
    - Event-based packing algorithm is most efficient in all cases
    - Impact of Packing:
      - test\_loadBalanced: Minimal impact due to majority of particles reaching end of simulation
      - test\_loadUnbalanced: Significant impact with speedup of up to 1.41 compared to history-based algorithm and up to 1.22 compared to event-based one.

![](_page_13_Figure_12.jpeg)

Comparison of the 3 particle-following GPU implementations - 1 Millions markers - 1 A100 Comparison of the 3 particle-following GPU implementations -10 Millions markers - 4 A100

#### ✓ [1830780] asco CUDA API step fo yoa 38 op 2.81599 2.81887s step fo vpa 38 gpr 2.82163 step to ypa 38 gpr itep fo vpa 38 gpu 2.82438 step\_fo\_vpa\_38\_gpu 2.82714 step fo vpa 38 gp 82988 step\_fo\_vpa\_38\_gp step\_fo\_vpa\_38\_gp step fo vpa 38 gp 2.84334 2.846025 step\_fo\_vpa\_38\_gpu CPU (32 CUDA HW (0000-14-00.0 99.6% Context 1 All Streams + 99.1% Kerne 100.0% 0.9% Memor 99.1% Stream 1 0.9% Stream 17 Memory usage 0.4% Unified mem Local Nemory Poo Static memory u Threads (10) V [1045479] CUDA API

Lower Local Memory Use: Event-based packing uses multiple smaller kernels, reducing local memory demands versus the history-based version.

- Efficient Data Transfer: Minimal data transfer overhead as all kernels run on the GPU.
- Optimized Memory Access: Contiguous, coalesced memory access through packing enhances efficiency.
- Reduced Loop Bounds: Through packing step, dynamic loop bounds improve runtime performance, with only ~30% particles active per timestep.

**EPFL** Profiling Nsys

![](_page_14_Figure_7.jpeg)

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### **EPFL** Profiling

![](_page_15_Picture_1.jpeg)

![](_page_15_Picture_2.jpeg)

#### EventBased version:

- kernels mostly memory-bound
- multiple branch divergences in end\_condition kernel involving lower Memory SOL due to thread divergence

| Main kernels              | %     |
|---------------------------|-------|
| move_particle             | 64.8  |
| diagnostics               | 9.6   |
| end_condition             | 6.5   |
| collisions                | 5.8   |
| copy_particles_structures | 5.5   |
| sorting                   | < 0.1 |
| packing                   | < 0.1 |

TABLE I.RELATIVE WEIGHTS OF THE DIFFERENT STEPS OF THE SIMULATION ON A100. % Values Are Averaged Simulating 1 Million Particles With The Ascot5 Event-Based-Packing Algorithm

| Main kernels  | Memory<br>SOL (%) | Compute<br>SOL (%) |
|---------------|-------------------|--------------------|
| move_particle | 68                | 30                 |
| diagnostics   | 80                | 26                 |
| end_condition | 36                | 12                 |
| collisions    | 40                | 56                 |

 $\label{eq:table_integration} \begin{array}{c} \mbox{table in Test\_loadBalanced}, \mbox{Speed Of Light - 1 Million Particles With} \\ \mbox{The Ascot5 Event-Based-Packing Algorithm} \end{array}$ 

## EPFL Benchmarks

![](_page_16_Picture_1.jpeg)

- 10M markers Benchmark:
  - Collisional full-orbit simulation of prompt-losses of fusion alpha particles
  - 2D wall; ITER-like but circular equilibrium interpolated with cubic splines
  - 2D wall rectangular, coulomb collisions, gyro orbit, simulation time = 0.0001s, fixed time step
  - Jed: 2x Platinum 8360Y, intel/2021.6.0
  - Leonardo: A100, nvhpc/23.1
  - NVIDIA Grace Hopper Superchip engineering sample early access courtesy of NVIDIA
  - NVIDIA Grace-Grace
  - Intel Ponte-Vecchio 600W engineering sample early access courtesy of INTEL

![](_page_16_Figure_11.jpeg)

#### **EPFL** Conclusion

![](_page_17_Picture_1.jpeg)

![](_page_17_Picture_2.jpeg)

- **Successful GPU Transition**: ASCOT5 was efficiently ported from CPU to GPU using a directive-based strategy, ensuring code consistency.
- **Optimized Algorithms**: Three strategies were tested, with event-based-packing achieving the best performance due to improved load balancing and reduced thread divergence.
- **Significant Speedup**: Event-based-packing on H100-96GB shows up to 6x speedup over a dual Intel Xeon CPU node.
- ACOT5-GPU is now fully imported into the master version
- Several groups have started using it
- Future Work: Conduct new tests incorporating enhanced physical models.